













Full	Ad	der			
	x	у	z	s	с
	0	0	0	0	0
	0	0	1	0	1
	0	1	0	0	1
	0	1	1	1	0
	1	0	0	0	1
	1	0	1	1	0
	1	1	0	1	0
	1	1	1	1	1
	Week3			<u> </u>	8































































What if more th Ignore "lower	nan o prio	one	inp " in	ut li puts	ne l	= ľ nas)C(a val	UDEIS
Note that pola	rity	of lo	<u>inpo</u> <u>ile i</u> Inr	s op S op	opos	site	from	Table 4-8 in Mano
I ₀	I 1	I ₂	I ₃	I ₄	I 5	I 6	I 7	$y_2 y_1 y_0$ Idle
0	0	0	0	0	0	0	0	x x x 1
1	0	0	0	0	0	0	0	0 0 0 0
Х	1	0	0	0	0	0	0	0 0 1 0
Х	Х	1	0	0	0	0	0	0 1 0 0
X	Х	X	1	0	0	0	0	0 1 1 0
X	X	X	X	1	0	0	0	1 0 0 0
• • • • • • • • • • • • • • • • • • • •	X	X	X	X	l	0	0	
X	x	Х	Х	Х	X	1	0	
X X X	X X	37	37	37	• • •	• • •		

























	Verilog	
and 0 1 x z 0 0 0 0 0 1 0 1 x x x 0 x x x	Or 0 1 x z 0 1 1 x x 1 1 1 1 1 x x 1 x x	
z 0 x x x	z x 1 x x not Input output	
xor 0 1 x z	0 1	
0 0 1 0 0	1 0	
1 10××	x x	
x x x x x	z x	
Z XXXX	Week3	53















